

Large Q-Factor Improvement for Spiral Inductors on Silicon Using Proton Implantation

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Abstract—We have improved the Q-factor of a 4.6 nH spiral inductor, fabricated on a standard Si substrate, by more than 60%, by using an optimized proton implantation process. The inductor was fabricated in a 1-poly-6-metal process, and implanted after processing. The implantation increased the substrate impedance by \sim one order of magnitude without disturbing the inductor value before resonance. The S-parameters were well described by an equivalent circuit model. The significantly improved inductor performance and VLSI-compatible process makes the proton implantation suitable for high performance RF ICs.

Index Terms—Implantation, inductor, Q-factor, RF.

I. INTRODUCTION

SILICON technology supports the needs of the rapidly growing communication market because of its low cost, high package density and mature technology. The widely used inductors on Si wafers, especially those for impedance matching and LC tanks in RF CMOS circuits, have poor quality (Q)-factors. This is mainly due to the high substrate RF loss of the low resistivity (10 Ω -cm) Si wafers [1]–[9] and the ohmic loss of the spiral metal. Since the ohmic loss can be reduced by using thick metal lines and highly conductive copper, the substrate loss becomes the dominant factor for the inductor's Q-factor. Several methods have been proposed to overcome this problem, such as isolating the substrate by using a thick dielectric layer [1] or a MEMS-etched air-gap below the inductors [2]. However, these nonconventional VLSI processes require additional process steps and package costs. In this paper, we have successfully improved the inductor's Q-factor using an optimized proton implantation process compatible with current VLSI technology [4]–[6]. The ion implantation process [3]–[9] is implemented after device fabrication and transforms the standard 10 Ω -cm Si substrate into high resistivity ($\sim 10^6$ Ω -cm) material [6] similar to semi-insulating

GaAs. The process enhanced the Q-factor by 61% without changing the inductance value, which demonstrates the benefits of using this simple VLSI-compatible implantation process.

II. EXPERIMENTAL PROCEDURE

A conventional standard 0.18 μ m 1-Poly-6-Metal (1P6M) Al-SiO₂ CMOS technology was used to fabricate the inductors and the simplified cross section is shown in Fig. 1(a). The substrate was the standard low-resistivity ~ 10 Ω -cm Si wafers used in a conventional CMOS process. The last metal (M6) is 2 μ m thick which reduces the metal ohmic loss of the spiral inductor, while the thickness of other metal layers is 0.5 μ m. Each inter-metal layer (IML) thickness is 1.1 μ m which is composed of two dielectric layers with dielectric constants of 3.7 and 4.2. The spiral inductor has 3.5 turns and a 166 μ m inner diameter with a line width of 10 μ m and a spacing of 5 μ m. After fabrication the inductors, a proton implantation process was applied with energy of ~ 4 MeV from a synchrotron ion source [3]–[9]. Compared with our previous implantation scheme, which was applied before device fabrication [3]–[9], the present approach, performed after full wafer fabrication, avoid any contamination to the VLSI process line. We measured the RF performance of the inductors with HP8510C Network Analyzer and performed standard de-embedding to remove the parasites associated with the pads.

III. RESULTS AND DISCUSSIONS

Fig. 1(b) shows the measured and modeled S-parameters for RF inductor fabricated using 1P6M process, where the modeled data is from the equivalent circuit and extracted values shown in Fig. 1(c). The slightly different S-parameters after implantation are due to the reduction of substrate impedance as explained by the equivalent circuit model analysis later. The inductor model consists of a series inductor L , series resistor R_s and a parallel capacitance C from the capacitive coupling of spiral turns. The C_{ox1} and C_{ox2} represent the capacitors from isolation oxide underneath the inductor, which include both inter-metal dielectric and trench isolation oxide. The C_{sub} and R_{sub} represent the RF loss path to ground. Except the substrate resistance (R_{sub}), the component values in the models are almost the same for inductors with and without proton implantation. Good agreement between measured and modeled S-parameters is obtained for the inductors with or without the proton implantation, indicating the accuracy of the physically-based circuit model that can also be used for substrate loss analysis.

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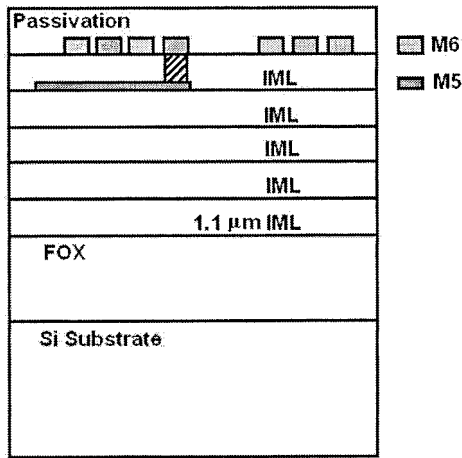
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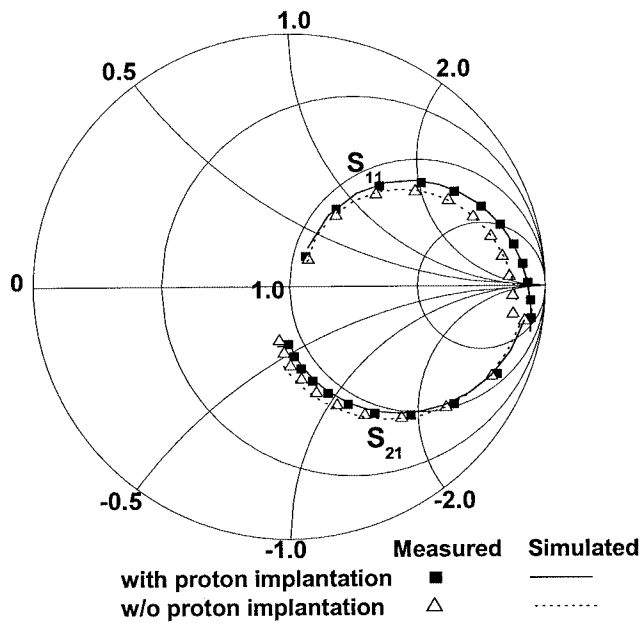
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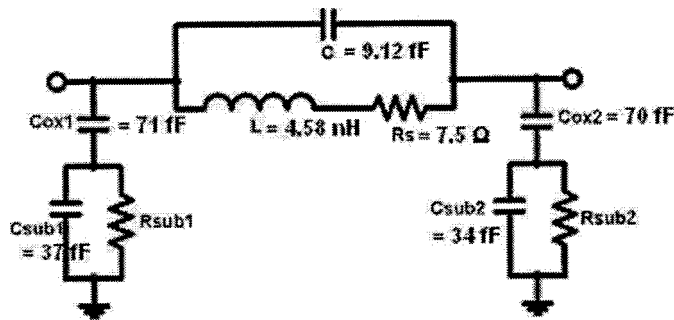
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(a)



(b)



(c)

Fig. 1. (a) Simplified cross section of spiral inductors using 1P6M Al-SiO₂ CMOS Technology. (b) The simulated and measured S-parameters up to 10 GHz, for inductors with and without the proton implantation. (c) The equivalent circuit model with component values for the inductors.

Fig. 2 shows the deduced inductance from the measured de-embedded S-parameters [10]–[12] with or without the proton implantation. The inductance of 4.6 nH is the same for both cases indicating that the proton implantation has almost no influences on the inductance value over a large frequency range.

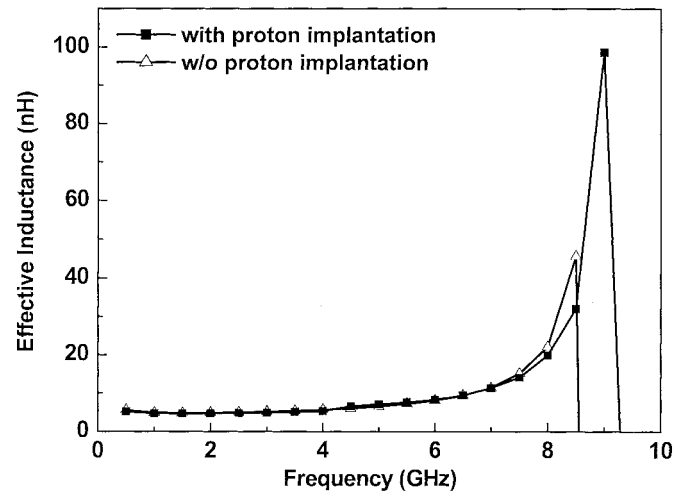


Fig. 2. Inductance values with and without the proton implantation.

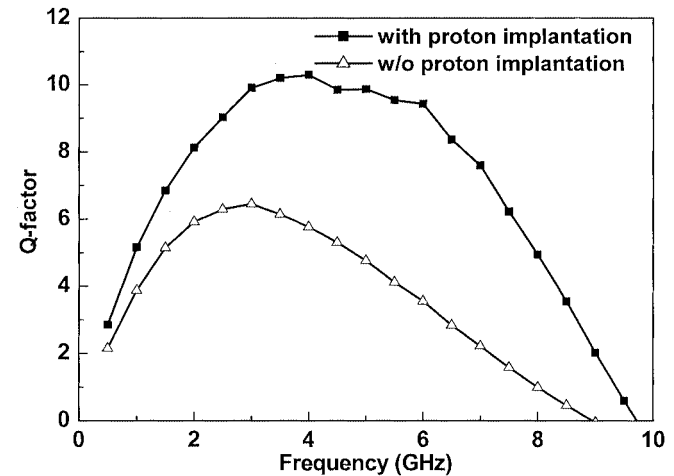


Fig. 3. Q-factor for the inductors, with and without the proton implantation.

This suggests that the proton implantation does not disturb the magnetic field around the inductor, up to at least 7 GHz, before self-resonance [13], [14]. Following implantation, the self-resonant frequency was ~ 9 GHz, which is 7% higher than the original value.

Fig. 3 shows the Q-factors for the inductor with and without the proton implantation. The inductor without the implantation has the max. Q-factor of 6.3 at 3 GHz, whereas the proton-implanted device peaks at 4 GHz with a Q-factor of 10.6. In addition to the 61% peak Q-factor improvement, the high Q-factor region is simultaneously extended to 6.2 GHz by using the using implantation process. This broader high Q-factor frequency region enables the 4.6 nH inductor to be useful for Wireless LAN (5.2–5.8 GHz).

To analyze the RF performance improvement further we have plotted in Fig. 4 the substrate impedance $|Z_{\text{sub}}|$ (parallel R_{sub} and C_{sub}), extracted from the equivalent circuit model, in Fig. 1(b). The substrate impedance is improved by \sim one order of magnitude, which is also observed in our previous transmission line [4], filter [3] and antenna [5] works. The reduced RF loss of inductor after implantation also explains the improved Q-factor and resonance frequency, shown in Figs. 2 and 3.

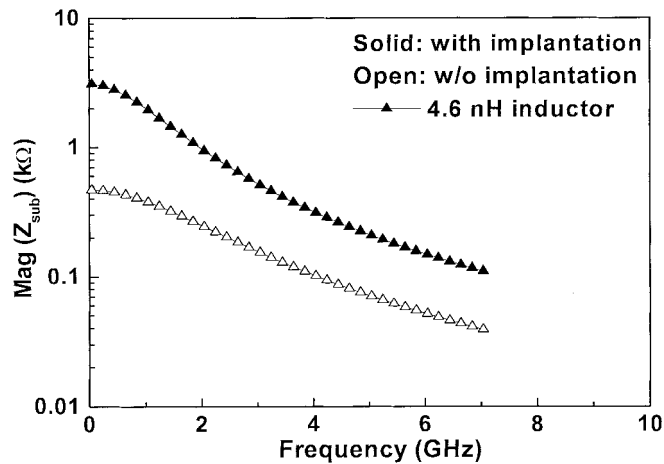


Fig. 4. Substrate impedance change before and after the proton implantation.

IV. CONCLUSION

Using a proton implantation process applied after inductor fabrication, a large Q -factor improvement of 61% has been obtained in an inductor made in a Si process. Simultaneously the high Q frequency region was extended to 6.2 GHz. This large improvement resulted from the larger substrate impedance. This was determined by extraction from an equivalent circuit model. This simple implantation process shows great potential for high- Q inductors in future applications at high frequencies.

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